

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	41752	((central adj board) CPU processor) same (active inactive)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/24 16:31
L2	3208	((central adj board) CPU processor) same ((active inactive) adj state)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/24 16:32
L3	18	((central adj board) CPU processor) same ((active inactive) adj state) same (frame and bit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/24 16:40
L4	168	((central adj board) CPU processor) same ((active inactive) adj state) same synchron\$10)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/24 16:45
L5	1489	((central adj board) CPU processor) same ((active inactive) adj state) and synchron\$10)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/24 16:45
L6	44	((central adj board) CPU processor) same ((active inactive) adj state) and synchron\$10 and (sonet sdh))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/24 17:01
L7	289	((central adj board) CPU processor) same ((active inactive) adj state) and synchron\$10 and (hierarch\$10 sonet sdh))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/24 17:05
L8	79203	((hierarch\$10 near\$5 network) sonet sdh) same ((central adj board) cpu processor) same (peripheral input output i/o)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/24 17:07
L9	255502	((hierarch\$10 near\$5 network) sonet sdh) same interface	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/24 17:07
L10	158	370/408.ccls. and ((hierarch\$10 near\$5 network) sonet sdh) same interface)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/24 17:13
L11	12	370/408.ccls. and ((sonet sdh) same interface)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/24 17:48
L12	4	370/408.ccls. and ((sonet sdh) same ((central adj board) cpu processor))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/24 17:48

L13	11	370/408.ccls. and ((sonet sdh) same ((central adj board) cpu processor switch))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/24 17:45
L14	69	370/539.ccls. and ((sonet sdh) same interface)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/24 18:01
L15	28	370/539.ccls. and ((sonet sdh) same ((central adj board) cpu processor))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/24 17:48
L16	67	370/539.ccls. and ((sonet sdh) same ((central adj board) cpu processor switch))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/24 17:59
L17	4	370/539.ccls. and ((sonet sdh) same ((central adj board) cpu processor switch) same (peripheral i/o))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/24 18:00
L18	4	370/539.ccls. and (((hierarchical adj network) sonet sdh) same ((central adj board) cpu processor switch) same (peripheral i/o))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/24 18:00